



SHRI DATTA SHETKARI SAHAKARI SAKHAR KARKIDANA LTD. CHARITABLE TRUSTS
APPASAHEB ALIAS SA. RE. PATIL
INSTITUTE OF TECHNOLOGY
APPROVED BY AICTE, NEW DELHI, DTE, MUMBAI, GOVT. OF MAHARASHTRA
AFFILIATED TO DBATU, LONERE, AFFILIATED TO MSBTE, MUMBAI

Department of Electrical Engineering
Summer Practical Examination 2026

Second Year B. Tech Sem IV

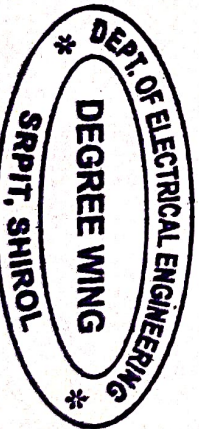
POE Time Table

As per revised schedule of DBATU, Practical /Oral Examination (POE) of even semester A. Y. 2025-26, is arranged as follows:

Sr. No	Course	Course Code	Day & Date	Time			Venue	Student Count
				BATCH NO	BATCH NO	BATCH NO		
1	Power System Lab	25AF1293PCL404	Monday 01/06/2026	10.00 am -12.00 pm	BATCH 1	Electrical Seminar Hall Room No B188	25	
				12.30 pm - 2.30 pm	BATCH 2		25	
				2.45 pm - 4.45 pm	BATCH 3		26	
2	Electrical Machine I Lab	25AF1293PCL412	Tuesday 02/06/2026	10.00 am -12.00 pm	BATCH 1	D.C Machine Lab Room No B181	25	
				12.30 pm - 2.30 pm	BATCH 2		25	
				2.45 pm - 4.45 pm	BATCH 3		26	
3	Analog and Digital Electronics Lab	25AF1293PCL413	Thursday 03/06/2026	10.00 am -12.00 pm	BATCH 1	PCB Lab Room No 152	25	
				12.30 pm - 2.30 pm	BATCH 2		25	
				2.45 pm - 4.45 pm	BATCH 3		26	

Important Instructions for students :

1. Students must be present 30 minute before to start exam
2. All students should come with college uniform with signed I-Card
3. All students must complete your Journals /Assignments with all signs and certificates
4. Students must be complete scholarship forms/ fees, before appearing to POE
5. Attendance is compulsory for all students



[Signature]
Exam Coordinator

[Signature]
Head Of the Department

[Signature]
Mrs. S. N. Pawar
Dean Engineering

[Signature]
Dr. D. N. Mudgal
Principal

Department of Electrical Engineering

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Dean Engineering

Principal